

Attorney's Docket No. 5649-877

*1-2502*  
*m*  
**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hag-ju Cho et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS  
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND  
METHODS OF FABRICATING SAME

Date: August 7, 2001

BOX PATENT APPLICATION

Commissioner for Patents

Washington, DC 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,

*Robert M. Meeks*  
Robert M. Meeks

Registration No. 40,723



20792

PATENT TRADEMARK OFFICE

**Certificate of Mailing under 37 CFR 1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to BOX PATENT APPLICATION, Commissioner for Patents, Washington, DC 20231, on August 7, 2001.

Signature: *Candi L. Riggs*  
Candi L. Riggs

Date of Signature: August 7, 2001